| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|----------|--------|---|---|---------------------|---------|------------------|
| L2 | 128603 | ((system adj1 on adj1 chip) or system-on-chip or SOC) and (core or basis or essence or hub) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR · | ON | 2005/08/29 09:20 |
| L3 | 6774 | ((pin or port or terminal) with (core or basis or essence or hub)) and 2 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/08/29 09:23 |
| L4 | 1092 | ((pin or port or terminal) near6 (compatib\$5 or match\$3 or equivalen\$2 or identical)) and 3 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/08/29 08:59 |
| L5 | 1 | ((binary adj1 decision adj1 diagram) or BDD) and 4 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/08/29 09:07 |
| L6 | 12 | ((binary adj1 decision adj1 diagram) or \$3BDD) and 4 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/08/29 09:06 |
| L7 | 19 | ((binary adj1 decision adj1 diagram) or \$3BDD) and 3 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/08/29 09:07 |
| L8 | 12 | ((binary near3 diagram) or \$3BDD) and 4 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/08/29 09:07 |
| L9 | 109 | ((binary adj1 decision adj1 diagram) or BDD) and 2 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/08/29 09:22 |
| L10 | 10 | ((pin or port or terminal) same (core or basis or essence or hub)) and 9 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/08/29 09:09 |
| L11 | 52 | (pin or port or terminal) and (core or basis or essence or hub) and 9 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/08/29 09:09 |
| L12 | 4 | "716"/\$.ccls. and 11 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR · | ON | 2005/08/29 09:09 |
| L13 | 0 | ((pin or port or terminal) near6 (compatib\$5 or match\$3 or equivalen\$2 or identical)) and 12 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/08/29 09:25 |

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|-----|----|---|---|----|----|------------------|
| L14 | 1 | ((pin or port or terminal) near6 (compatib\$5 or match\$3 or equivalen\$2 or identical)) and 11 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/08/29 09:19 |
| L16 | 68 | (((system adj1 on adj1 chip) or system-on-chip or SOC) and (core or basis or essence or hub)).clm. | US-PGPUB | OR | ON | 2005/08/29 09:24 |
| L17 | 0 | ((binary adj1 decision adj1 diagram) or \$3BDD) and 16 | US-PGPUB | OR | ON | 2005/08/29 09:35 |
| L18 | 24 | (pin or port or terminal).clm. and (core or basis or essence or hub).clm. and 16 | US-PGPUB | OR | ON | 2005/08/29 09:31 |
| L19 | 3 | ((pin or port or terminal) with (compatib\$5 or match\$3 or equivalen\$2 or identical)).clm. and 18 | US-PGPUB | OR | ON | 2005/08/29 09:27 |
| L20 | 3 | ((pin or port or terminal) same (compatib\$5 or match\$3 or equivalen\$2 or identical)).clm. and 18 | US-PGPUB | OR | ON | 2005/08/29 09:27 |
| L21 | 5 | ((pin or port or terminal) and (compatib\$5 or match\$3 or equivalen\$2 or identical)).clm. and 18 | US-PGPUB | OR | ON | 2005/08/29 09:32 |
| L22 | 3 | ((pin or port or terminalor input or output) and (compatib\$5 or match\$3 or equivalen\$2 or identical)).clm. and 18 | US-PGPUB | OR | ON | 2005/08/29 09:31 |
| L23 | 41 | (pin or port or terminal or input or output).clm. and (core or basis or essence or hub).clm. and 16 | US-PGPUB | OR | ON | 2005/08/29 09:31 |
| L24 | 6 | ((pin or port or terminal or output or input) and (compatib\$5 or match\$3 or equivalen\$2 or identical)).clm. and 23 | US-PGPUB | OR | ON | 2005/08/29 09:32 |
| L26 | 0 | ((binary adj1 decision adj1 diagram) or \$3BDD).clm. and 24 | US-PGPUB | OR | ON | 2005/08/29 10:12 |